Description

The DC0524S3 is a TVS array, utilizing leading monolith-ic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The DC0524S3 complies with the IEC 61000-4-2 (ESD) standard with ±30kV air and ±30kV contact dis-charge. It is assembled into a 6-pin lead-free SOT-363 package. The combination of small size, and high ESD surge capability make it ideal for use in applications such as USB 3.0, multimedia, and other high speed ports.

Features

- Low leakage current
- Operating voltage: 5V
- Low clamping voltage
- JEDEC SOT-363 package
- Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test
    - Air discharge: ±30kV
    - Contact discharge: ±30kV
  - IEC61000-4-5 (Lightning) : 8A(8/20μs)
- ROHS Compliant

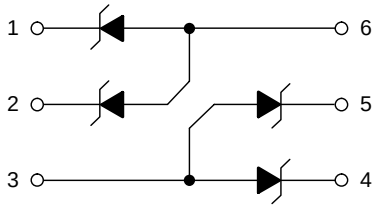
Mechanical Characteristics

- Package: SOT-363
- Lead Finish: Matte Tin
- Case Material: “Green” Molding Compound
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminal Connections: See Diagram Below
- Marking Information: See Below

Applications

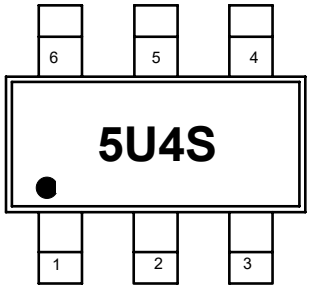
- Peripherals
- Industrial Equipment
- Notebook Computers
- Portable Instrumentation
- Microprocessor Based Equipment
- Cell Phone Handsets and Accessories
- Personal Digital Assistants (PDAs) and Pagers

Dimensions and Pin Configuration



Circuit and Pin Schematic

Marking Information



5U4S= Device Marking Code

Ordering Information

Part Number	Marking	Packaging	Reel Size
DC0524S3	5U4S	3000/Tape & Reel	7 inch

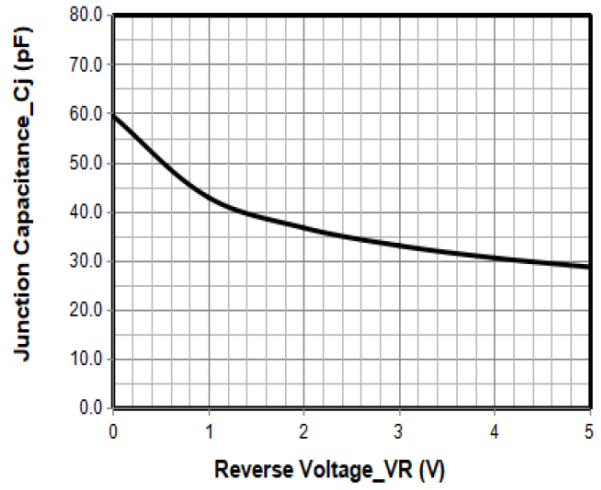
Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20μs)	Ppk	100	W
Peak Pulse Current (8/20μs)	I_{PP}	8	A
ESD per IEC 61000−4−2 (Air) ESD per IEC 61000−4−2 (Contact)	V_{ESD}	±30 ±30	kV
Operating Temperature Range	T_J	−55 to +125	°C
Storage Temperature Range	Tstg	−55 to +150	°C

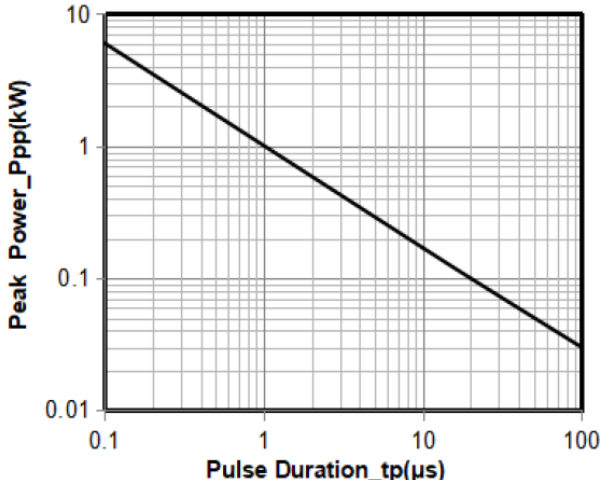
Electrical Characteristics (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V_{RWM}			5	V	
Breakdown Voltage	V_{BR}	6			V	I_T = 1mA
Reverse Leakage Current	I_R			0.2	μA	V_{RWM} = 5V
Clamping Voltage	V_C			9	V	I_{PP} = 1A (8 x 20μs pulse
Clamping Voltage	V_C			12.5	V	I_{PP} = 8A (8 x 20μs pulse)
Junction Capacitance	C_J		60		pF	VR = 0V, f = 1MHz

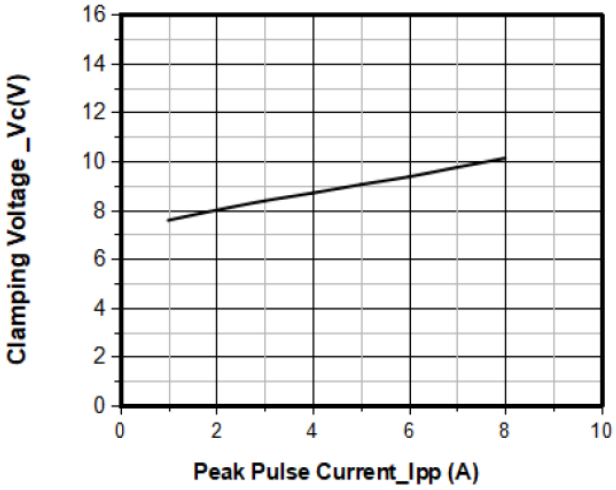
Typical Performance Characteristics (T_A=25℃ unless otherwise Specified)



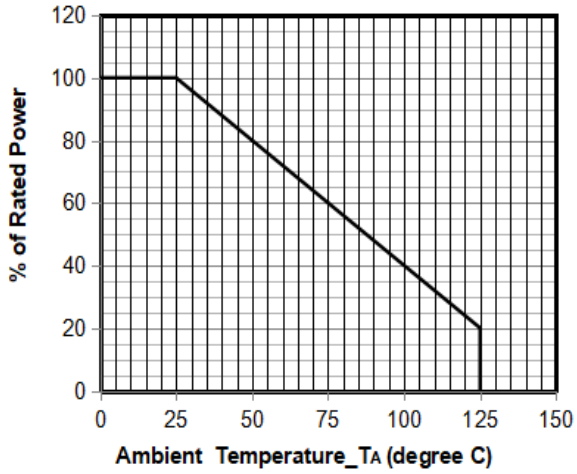
Junction Capacitance vs. Reverse Voltage



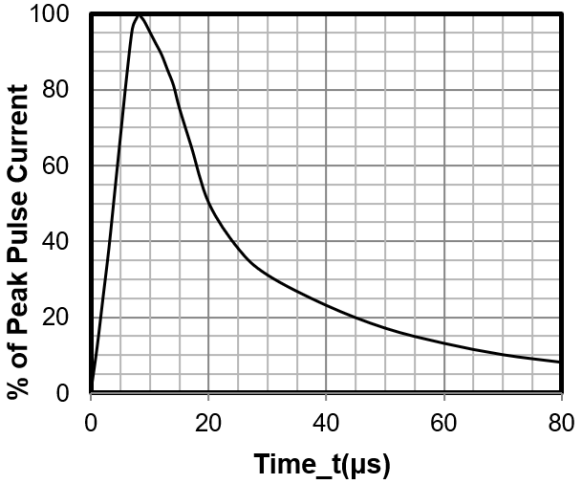
Peak Pulse Power vs. Pulse Time



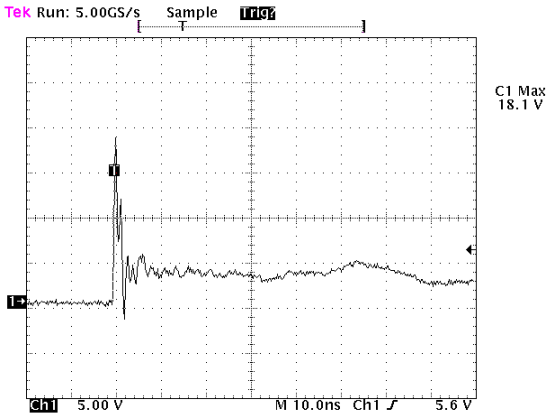
Clamping Voltage vs. Peak Pulse Current



Power Derating Curve



8 X 20μs Pulse Waveform

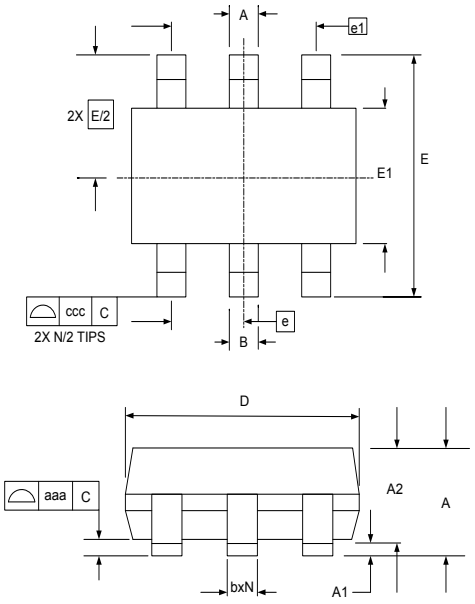


Note：Data is taken with a 10x attenuator

ESD Clamping Voltage

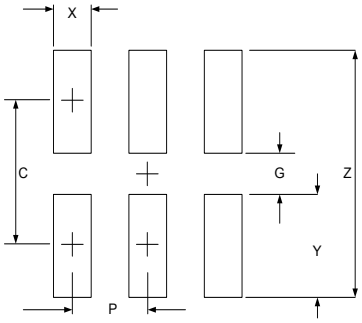
8 kV Contact per IEC61000−4−2

SOT-363 Package Outline Drawing



SYM	DIMENSIONS					
	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A			1.10			0.043
A1	0.00		0.10	0.000		0.004
A2	0.70	0.90	1.00	0.028	0.035	0.039
b	0.15		0.30	0.006		0.012
c	0.08		0.22	0.003		0.009
D	1.80	2.00	2.20	0.071	0.079	0.087
E1	1.15	1.25	1.35	0.045	0.049	0.053
E	2.10 BSC			0.083 BSC		
e	0.65 BSC			0.026 BSC		
e1	1.30 BSC			0.051 BSC		
N	6			6		
aaa	0.10			0.004		
ccc	0.30			0.012		

Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
C	1.85	0.073
G	1.00	0.039
P	0.65	0.026
X	0.40	0.016
Y	0.85	0.033
Z	2.70	0.106

Contact Information

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